



LT9611UX --- Product Brief

Dual-Port MIPI DSI/CSI to HDMI2.0 with MIPI Input Switch and Audio

Features

z Single/Dual-Port MIPI DSI/CSI Receiver

- f Compliant with D-PHY1.2 & DSI1.3 & CSI-2 1.3
- f Integrated DSC1.2 decoder
- f 4 physical ports grouped into 2 dual-port receivers, only one receiver activated at a time (2-to-1 switching internally)
- f 1/2 configurable ports per dual-port receiver
- f 1 clock lane and 1/2/3/4 configurable data lanes per port
- f 80Mbps~2Gbps per data lane
- f Skew calibration
- f Programmable receiver equalizer
- f Support data lane swap(arbitrarily) and polarity inversion(independent)
- f 3D support: two ports simultaneously receiving L and R frames or odd-L/even-R alternative pixels
- f DSI support both burst mode and non-burst mode
- f DSI support video formats:

DSC/CSC disabled: Packed 16/18/24/30/36.8(th bud*RGB,d)JTJ- 118383-1-7267 TD-0341 To 0000 Tty L and Paper 6/2

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z Digital Audio Input

- f I2S interface supporting 8-channel audio, with sample rates of 32~192 kHz and sample sizes of 16~24 bits
- f SPDIF interface supporting PCM, Dolby Digital, DTS digital audio at up to 192kHz frame rate
- f IEC60958 or IEC61937 compatible

z HDMI2.0 Transmitter

- f Compliant with HDMI2.0b, HDMI1.4 and DVI1.0
- f Compliant with HDCP2.2 and HDCP1.4
- f Data rate up to 6Gbps
- f On-die back termination
- f Programmable transmitter swing and pre-emphasis
- f AC-couple capable
- f Support 1 channel 0-341 To 0000 Tty L and Paper 6/2
- f Support 4k@60Hz
- f Supported 3D formats: side-by-side(full)

Packed 16/24-bit YCbCr4:2:2, Packed 20-bit

YCbCr4:2:2 DSC/CSC disabled: 24/30/36-bit RGB, 16/20/24-bit

DSC enabled, CSC disabled: Packed 24-bit RGB,

Packed 16-bit YCbCr4:2:2, Packed 12-bit YCbCr4:2:0

DSC/CSC enabled: Packed 24-bit RGB, Packed

16-bit YCbCr4:2:2 DSC enabled, CSC disabled: 24-bit RGB, 16-bit

f CSI support video formats, 8-bit YCbCr4:2:0

DSC/CSC enabled: 24-bit RGB/YCbCr4:4:4, 16-bit YCbCr4:2:2

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