



国民技术

N32H492xE/xG

N32H492	32 bit ARM Cortex-M4F				240MHz			DSP		
1MB	Flash	320KB SRAM	80KB CCM SRAM	+ 4KB Backup SRAM			3	12bit 4.7Mbps		
ADC 2	12bit DAC	USB FS Device	USB HS Host/Device	U(S)ART	I2C	SPI	CAN-FD	Ethernet		
	SDIO	FEMC	xSPI	SDRAM	I2S					
					AES/TDES		SHA	SM3	SM4	
MD5	TRNG		CRC16/32							

CPU

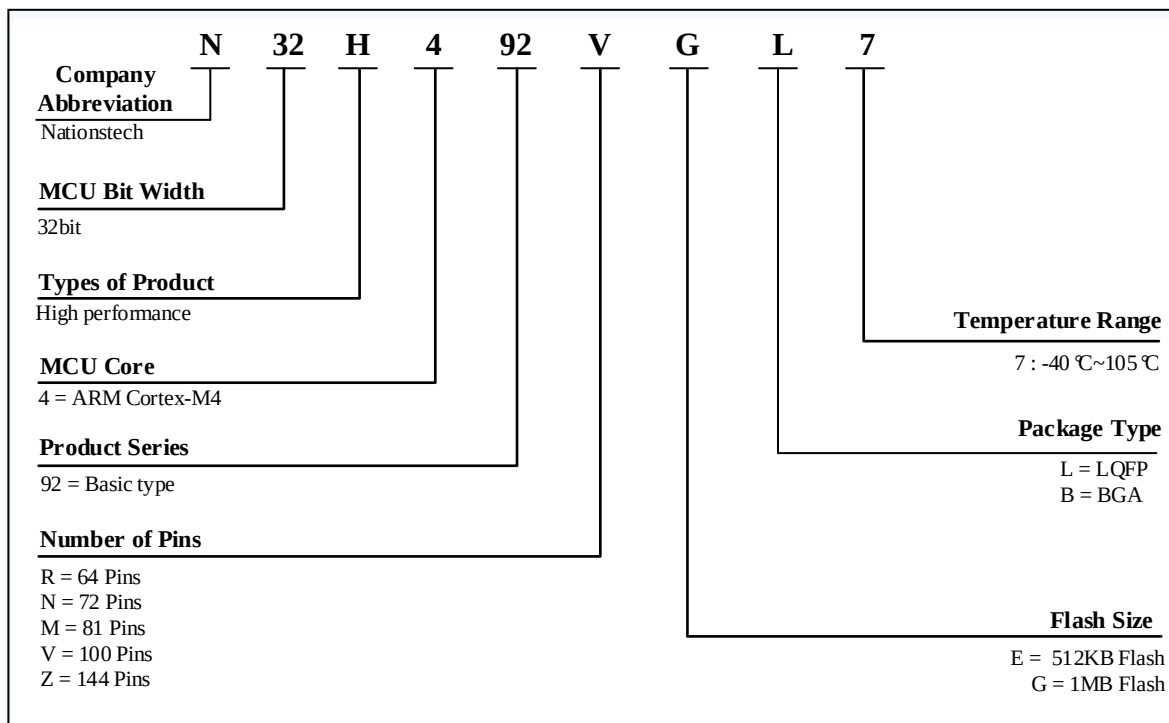
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118 GPIOs PD6~PD7 PG9~PG15 9 IO VDDIO

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(1)			(2)	SPQ ⁽³⁾	

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- 2.
- 3.

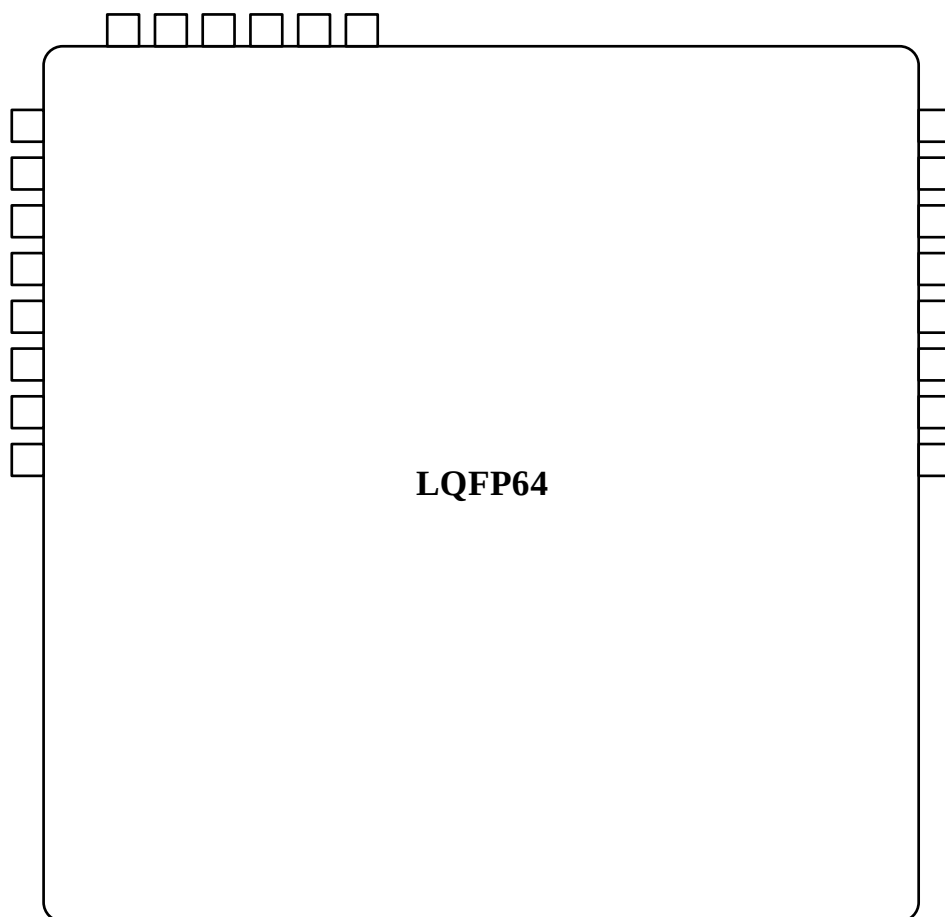
		N32H492RE/RG L7		N32H492RE/RG B7		N32H492NE/NG B7		N32H492ME/M GB7		N32H492VE/VG L7		N32H492ZE/ZG L7	
		1.8~3.6V/-											
CPU		ARM Cortex-M4F @240MHz, 300DMIPS											
Flash	KB	512	1024	512	1024	512	1024	512	1024	512	1024	512	1024
Total SRAM (KB)	General SRAM	240 ⁽¹⁾											
	CCM SRAM ⁽²⁾	80 ⁽³⁾											
	Backup SRAM	4											
	ATIM	3*16bit											
	GTIM	5*16bit											
		2*32bit ⁽⁴⁾											
		3*16bit ⁽⁵⁾											
	BTIM	2*32bit											
	LPTIM	2*16bit											
	SysTick timer	1											
	WWDG	1*14bit											

12bit ADC Number of channels	3 20	3 23	3 23	3 23	3 20	3 28
12bit DAC Number of channels	2 2					
	DES/3DES AES SHA1/SHA224/SHA256 SM3 SM4 MD5 CRC16/CRC32					
TRNG	Yes					
Cordic	Yes					
DSMU Number of channels	1 8					
	RDP/WRP					
	LQFP64	BGA64	BGA72	BGA81	LQFP100	LQFP144

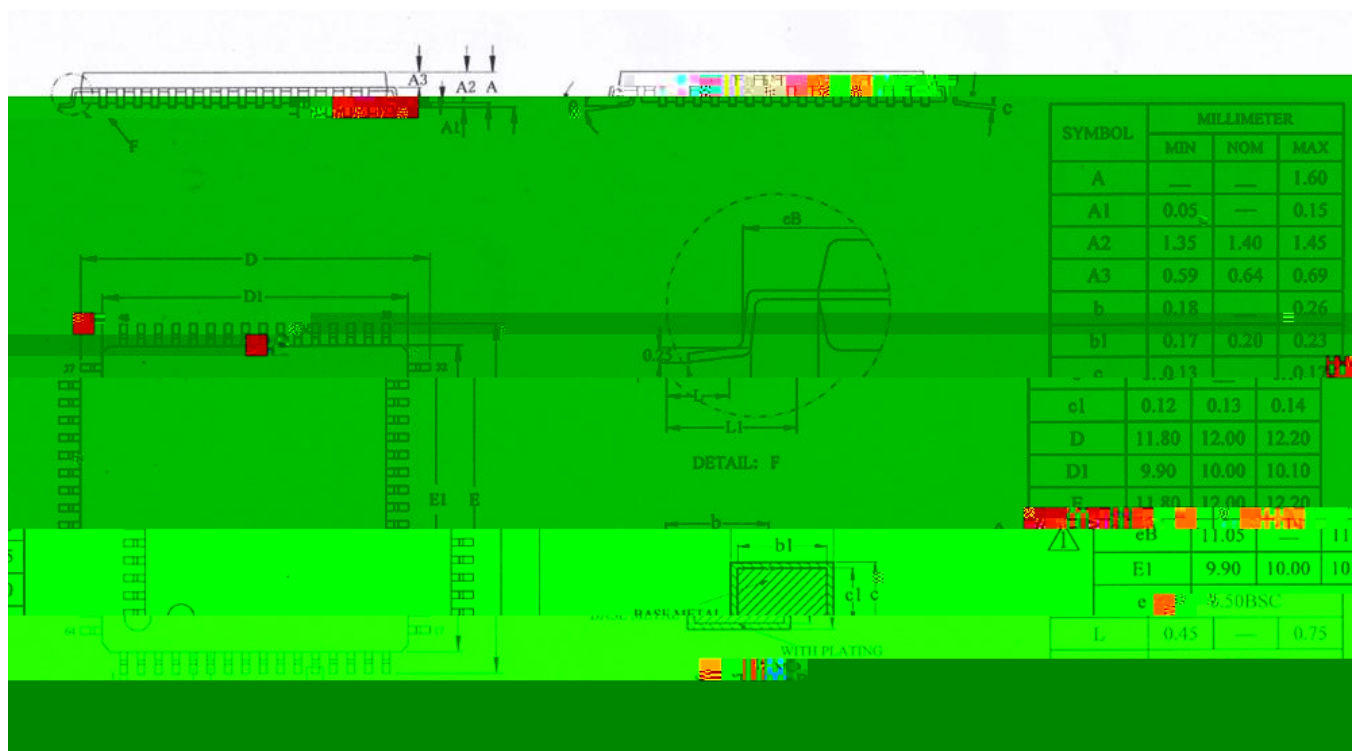
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3.1 LQFP64

3.1.1 LQFP64



3.1.2 LQFP64

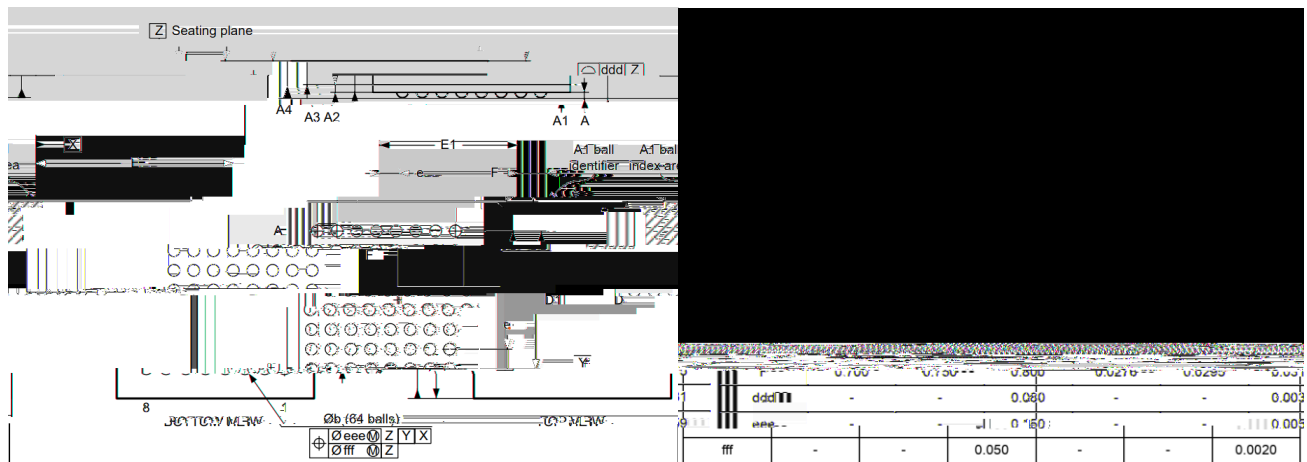




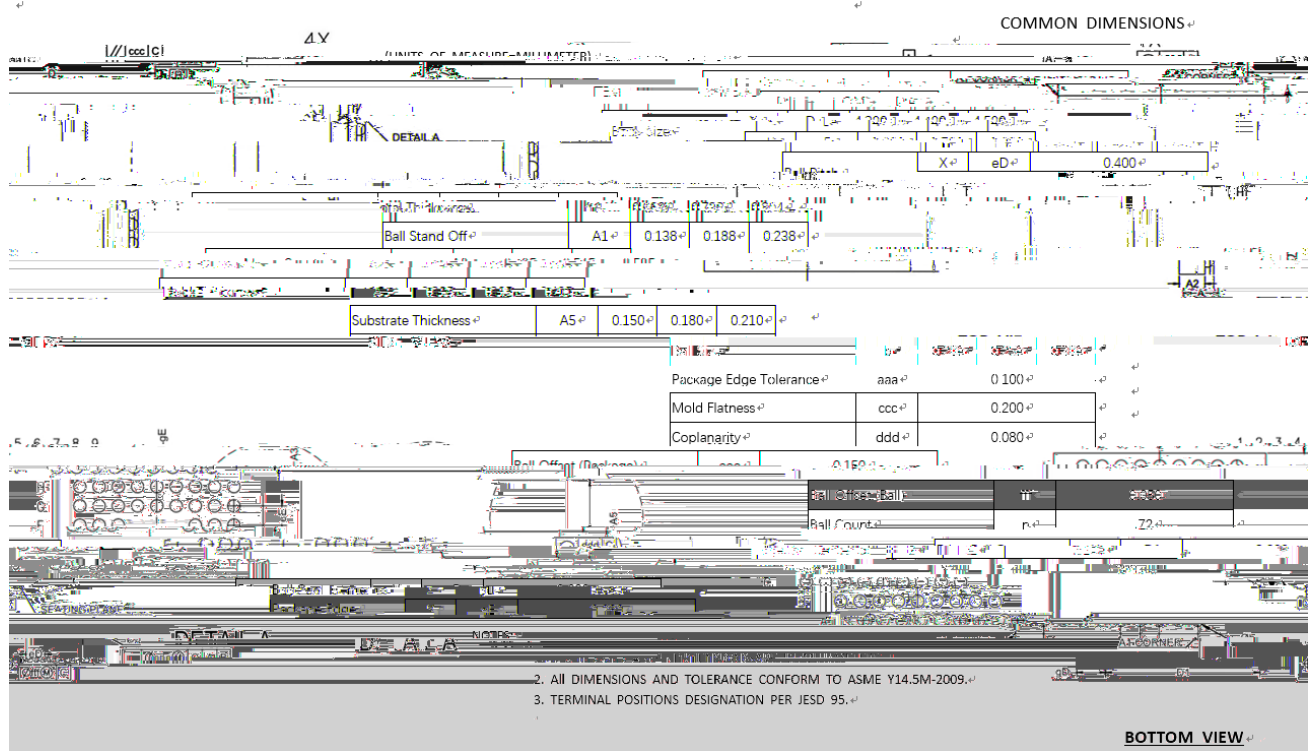
3.2 BGA64

3.2.1 BAG64

3.2.2 BGA64

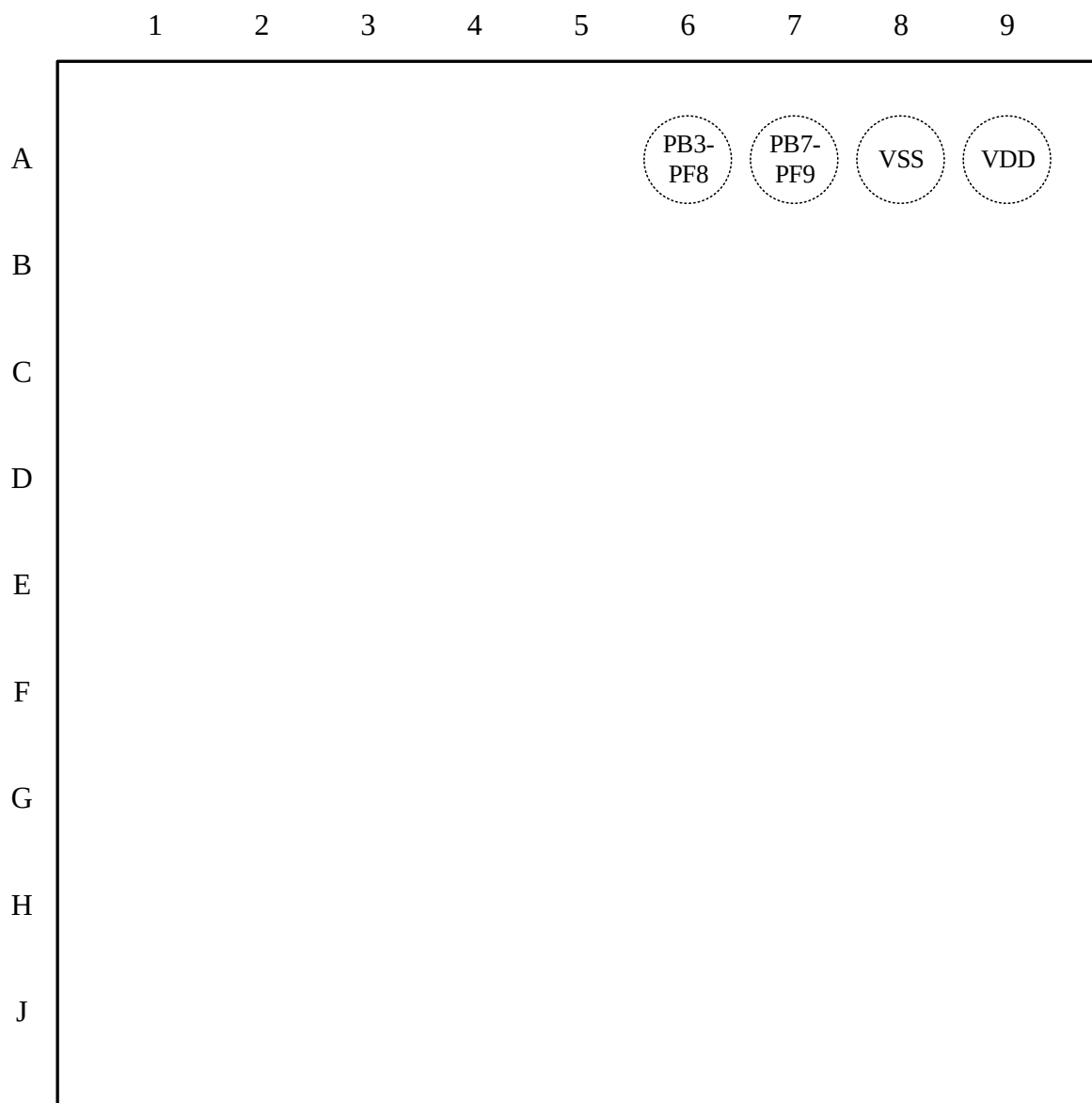


3.3.2 BGA72



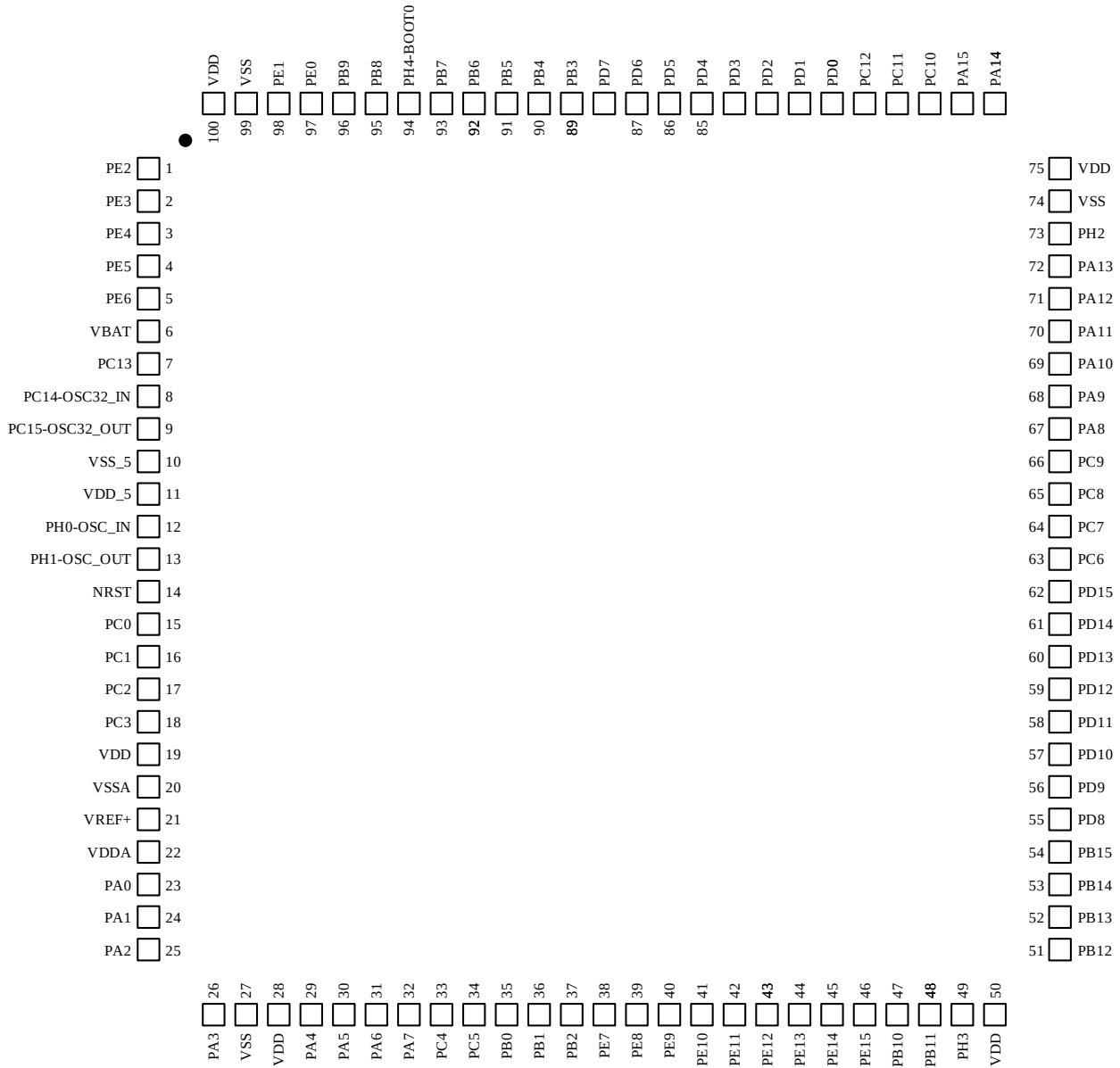
3.4 BGA81

3.4.1 BGA81



3.5 LQFP100

3.5.1 LQFP100

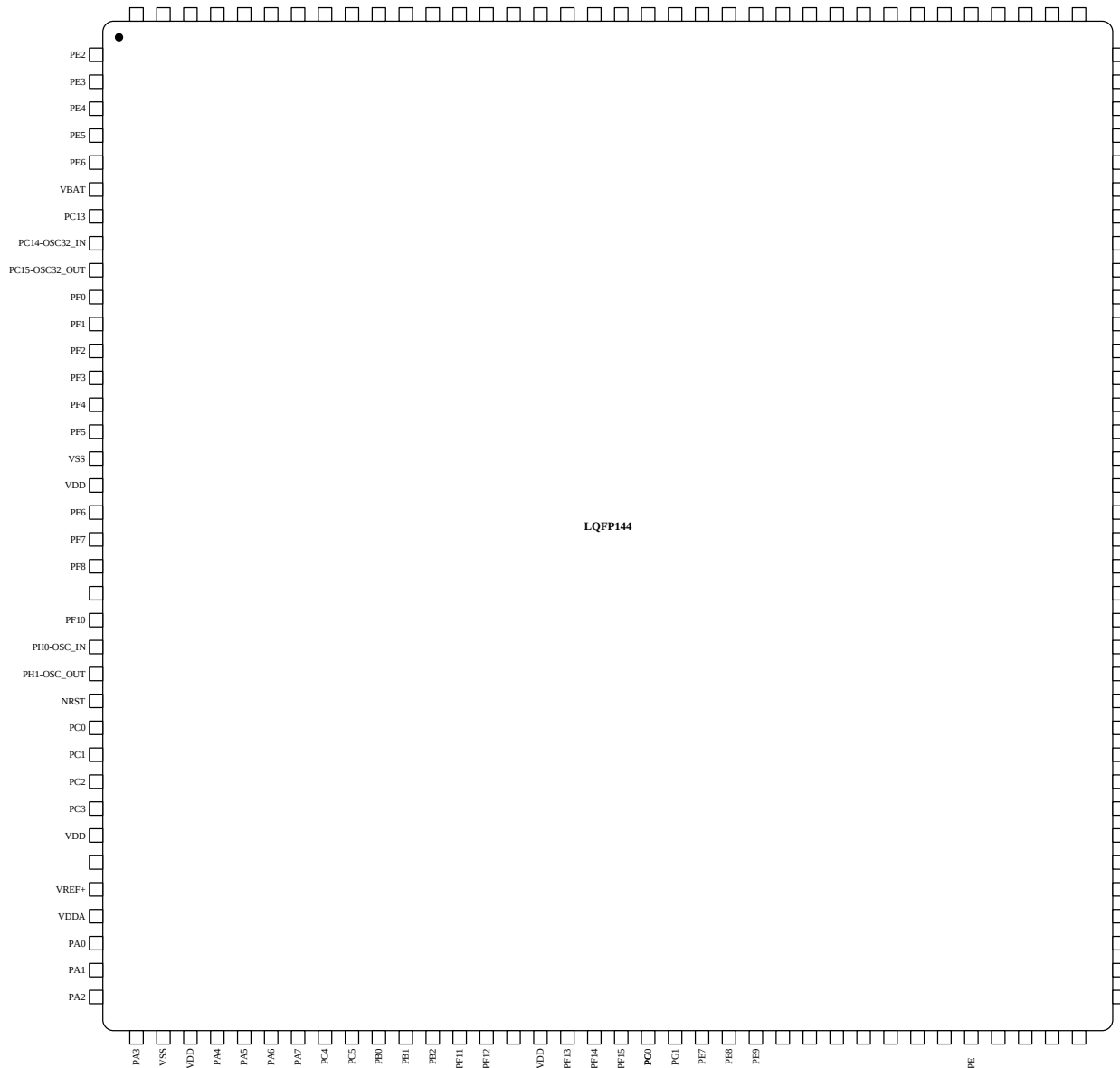


LQFP100

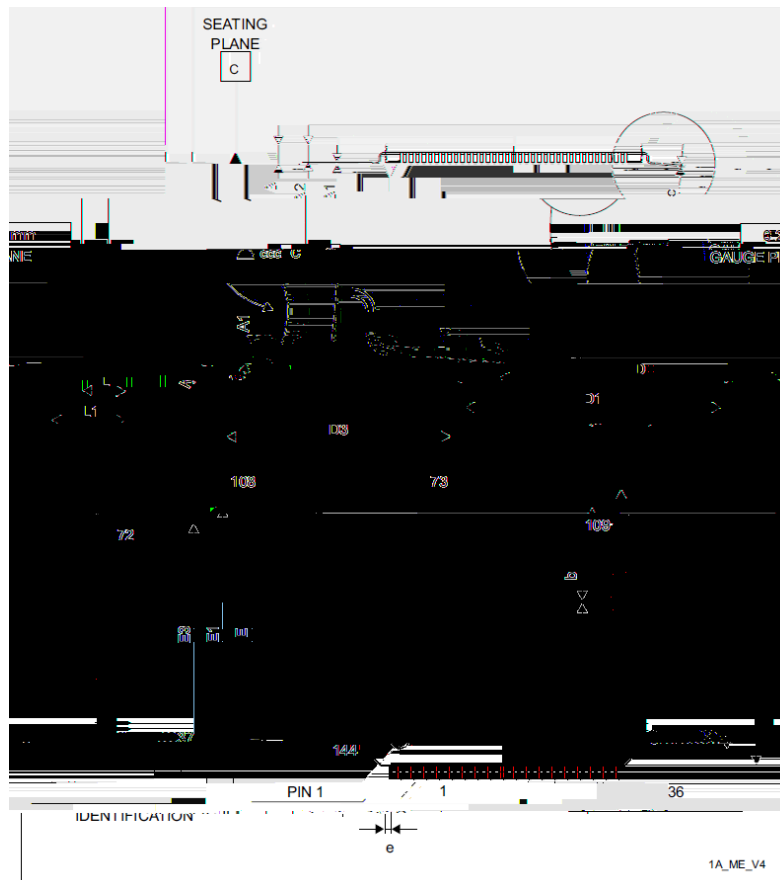


3.6 LQFP144

3.6.1 LQFP144

[illegible]

3.6.2 LQFP144

[illegible]

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